

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Simon Deleonibus CONFIRMATION NO.: 8398
SERIAL NO.: 10/539,928
FILING DATE: May 2, 2006
TITLE: MIS TRANSISTOR WITH SELF-ALIGNED GATE AND
METHOD FOR MAKING SAME
EXAMINER: Sarah Kate Salerno
ART UNIT: 2814

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND/OR REPLY TO OFFICE ACTION

Sir

In response to the Office Action mailed June 29, 2009, please amend the subject application as indicated.

Amendments to the Claims, if any, are reflected in the Listing of Claims beginning on page 2.

Remarks begin on page 5.